

J15 Turbo Switch	1 2	Turbo Signal Ground
J16 Hardware Reset	1 2	Ground Reset signal
J17 Speaker Connector	1 2 3 4	Speaker signal NC Ground +5V
J18 Keypad and Power LED Connector	1 2 3, 5 4	Power LED signal Spade Ground Keypad

Table 2-6. Connector Pin Definitions

VESA Bus Connector

The cache system board provides two high-performance VESA bus connectors, SL13 and SL14, for use with VESA peripherals. These connectors can be utilized for one Local Bus Master and one Local Bus Slave, either (SL13) or (SL14).

The following tables give the pin assignments for SL13 and SL14. Side A of the connector are pin outs on the board's component side while Side B are pin outs on the board's solder side. Jumpers JV2 and JV3 give more information on settings on the mainboard and the VL-bus controller.

JUMPER	PIN	DESCRIPTION
JV2	1-2	> 33MHz
	2-3	< or = 33MHz
JV3	1-2	1 WAIT WRITE
	2-3	0 WAIT WRITE

Table 2 - 7. VESA Bus Connector

CONNECTOR	SIDE A - PINS AND PIN OUTS	SIDE B - PINS AND PIN OUTS
SL13 — Local Bus Connector	01 02 03, 10, 17, 24, 35, 43, 51	DAT01 DAT03 Ground
	04 05	DAT05 DAT07
	06	DAT09
	07	DAT11
	08	DAT13
	09	DAT15
	11	DAT17
	12, 27, 40, 53	VCC
	13	DAT19
	14	DAT21
	15	DAT23
	16	DAT25
	18	DAT27
	19	DAT29
	20	DAT31
	21	DAT33
	22	DAT35
	23	DAT37
	25	DAT39
	26	DAT41
	28	DAT43
	29	DAT45
	30	DAT47
	31	DAT49
	32	DAT51
	33	DAT53
	34	DAT55
	36	DAT57
	37	DAT59
	38	DAT61
	39	DAT63
	41	DAT65
	42	DAT67
	44	DAT69
	45	DAT71
	48	DAT73
	49	DAT75
	50	DAT77
	52	DAT79
	54, 55, 56	DAT81
	57	DAT83
	58	DAT85

Table 2-8. Local Bus Connector Pin Assignment (Continued)